WEST Search History

DATE: Thursday, January 06, 2005

Hide?	Set Name	Query	Hit Count
		DB=JPAB; PLUR=YES; OP=OR	
☐	L12	(memory or RAM or DRAM) and (switch$ or alternat$) adj7 second clock	3
		DB=USPT; PLUR=YES; OP=OR	
☐	L11	L10 and (switch$ or alternat$) adj7 second clock	45
☐	L10	L7 and (switch$ or alternat$) adj7 first clock	113
☐	L9	L8 and (read$ or writ$ or access$) adj4 (RAM or DRAM or memory)	147
☐	L8	L7 and (switch$ or alternat$) adj6 (first clock or second clock)	169
☐	L7	L6 and clock signal	184
☐	L6	L5 and (switch$ or alternat$) adj7 (first clock or second clock)	226
☐	L5	L4 and memory	157773
☐	L4	RAM or DRAM	214512
		DB=DWPI; PLUR=YES; OP=OR	
☐	L3	l1 and arbitrat$	0
		DB=PGPB,USPT; PLUR=YES; OP=OR	
☐	L2	l1 and arbitrat$	13
☐	L1	(memory and (DRAM or RAM) and clock and (alternat$ or switch$) adj3 clock$ and read$ and writ$ and clock signal and synchroniz$ adj4 clock$ and (select$ or alternat$ or switch$) adj5 clock$ adj5 (first or second))	116

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER